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TRANSMITTAL FORM  (to be used for all correspondence after initial filing)		Applicat	ion Number	10/665,931	
		Filing Da	ate	September 19, 2003	
		First Na	med Inventor	Keiko Motokawa	
		Art Unit		Unassigned	
		Examine	er Name	Unassigned	
Total Number of Pages in This Submission		Attorney	Docket Number	16869S-094900US	
	ENC	LOSURES	(Check all that appl	у)	
		ng(s)		After Allowance Communication to	
Fee Attached Lic		icensing-related Papers		Appeal Communication to Board of and Interferences	
Amendment/Reply	☐ Petition			Appeal Communication to Group (A Notice, Brief, Reply Brief)	
After Final	Petition to Convert to Provisional Application			Proprietary Information	
Affidavits/declaration(s)	Power of Attorney, Revocation Change of Correspondence Address			Status Letter	
Extension of Time Request	Termin	al Disclaimer		Other Enclosure(s) (please identify below):	
Express Abandonment Request		est for Refund lumber of CD(s)		USPTO form SB08B (one page), copies cited references, Return Postcard	
Information Disclosure Statement				,	
Certified Copy of Priority Document(s)	Rema		The Commissioner is Account 20-1430.	authorized to charge any additional fees to	
Response to Missing Parts/ Incomplete Application					
Response to Missing Parts under 37 CFR 1.52 or 1.53		•			
			ANT, ATTORNEY,	OR AGENT	
Firm Townsend and Tor		nd Crew L			
Individual Robert C. Colwe	ell , , , , , , , , , , , , , , , , , ,		Reg. N	0. 27,431	
Signature / L	C Cor	ule			
Date に(と	103				
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Typed or printed name	Roger Hylton		
Signature	77-	Date	11/25/03

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## UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Keiko Motokawa, et al.

Application No.: 10/665,931

Filed: September 19, 2003

For: COMPILER AND METHOD FOR OPTIMIZING OBJECT CODES FOR HIERARCHICAL MEMORIES

Examiner: Unassigned

Art Unit: Unassigned

INFORMATION DISCLOSURE

STATEMENT UNDER 37 CFR §1.97 and

§1.98

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

The references cited on attached form PTO/SB/08B are being called to the attention of the Examiner. Copies of the references are enclosed. It is respectfully requested that the cited references be expressly considered during the prosecution of this application, and the references be made of record therein and appear among the "references cited" on any patent to issue therefrom.

As provided for by 37 CFR §1.97(g) and (h), no inference should be made that the information and references cited are prior art merely because they are in this statement and no representation is being made that a search has been conducted or that this statement encompasses all the possible relevant information.

Applicant believes that <u>no fee is required</u> for submission of this statement. However, if a fee is required, the Commissioner is authorized to deduct such fee from the undersigned's Deposit Account No. 20-1430. Please deduct any additional fees from, or credit any overpayment to, the above-noted Deposit Account.

Keiko MOTOKAWA, et al. Application No.: 10/665,931

Page 2

Respectfully submitted,

Robert C. Colwell Reg. No. 27,431

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RCC:rnh 60085070 v1

titute for form 1449B/PTO **INFORMATION DISCLOSURE** STATEMENT BY APPLICANT

(use as many sheets as necessary)

of

Sheet

C mplete if Known			
Application Number	10/665,931		
Filing Date	September 19, 2003		
First Named Inventor	Keiko Motokawa		
Art Unit	Unassigned		
Examiner Name	Unassigned		
Attorney Docket Number	16869S-094900US		

NON PATENT LITERATURE DOCUMENTS				
Examiner Initials *	Cite (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue num publisher, city and/or country where published.		T²	
	1	"Optimizing FORTRAN90, (HI-UX/MPP for SR8000)" user's guide page 6.5, Hitachi, Ltd. (1999).		
<del>-</del>	2	"XL Fortran for AIX User's Guide, Version 7 Release 1" pp. 192-195, IBM Corporation (2000).	√	
	3	DOWD "Memory Reference Optimization," in High Performance Computing, section 11.1, O'Reilly and Associates, Inc., Sebastopol, CA, (1993).		
	4	MOWRY et al. "Design and Evaluation of a Compiler Algorithm for Prefetching," Proceedings of the Fifth International Conference on Architectural Support for Programming Languages and Operating Systems, Boston, MA, pp. 62-73 (1992).		
	5	WOLF et al. "Improving Locality and Parallelism in Nested Loops," PhD thesis, Stanford University, Dept. of Computer Science (1992).		

Examiner	Date	
Signature	Considered	

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

Applicant's unique citation designation number (optional). Applicant is to place a check mark here if English language Translation is attached.